

Examiner-Initiated Interview Summary

Application No.

10/705,518

Applicant(s)

KIM, GI MUN

Examiner

Blane J. Jackson

Art Unit

2618

All Participants:(1) Blane J. Jackson.(2) Lew Edward V. Macapagal.**Status of Application:** _____

(3) _____

(4) _____

Date of Interview: 15 September 2006**Time:** 1300 EST**Type of Interview:**☒ Telephonic☐ Video Conference☐ Personal (Copy given to: ☐ Applicant ☐ Applicant's representative)Exhibit Shown or Demonstrated: ☐ Yes ☐ No

If Yes, provide a brief description:

Part I.

Rejection(s) discussed:

none

Claims discussed:

27-29

Prior art documents discussed:

*The IDS including Japanese publication number 4-75423 to Osaka***Part II.**

SUBSTANCE OF INTERVIEW DESCRIBING THE GENERAL NATURE OF WHAT WAS DISCUSSED:

*See Continuation Sheet***Part III.**

- ☐ It is not necessary for applicant to provide a separate record of the substance of the interview, since the interview directly resulted in the allowance of the application. The examiner will provide a written summary of the substance of the interview in the Notice of Allowability.
- ☐ It is not necessary for applicant to provide a separate record of the substance of the interview, since the interview did not result in resolution of all issues. A brief summary by the examiner appears in Part II above.

(Examiner/SPE Signature)_____
(Applicant/Applicant's Representative Signature – if appropriate)

Continuation of Substance of Interview including description of the general nature of what was discussed: It was suggested that the incorporation of new class 28 and 29 into independent new claim 27 would amend the claim to not read on the prior art, especially Osaka. Also, the introduction of "(schottky diode)" to identify "short key diode" into the Specification is considered new matter, consequently, delete all instances of "short key" or delete all instances of "short key" and insert "PIN" in the claims.

Do Not ENTER
Blam Jash
09/18/06

In the specification:

Please amend par. 29 as provided below:

[0029] The impedance matching units 30 and 50, respectively, may comprise one or more 50Ω resistance means (i.e., resistors) R1~R4, for example, in certain embodiments. The power unit 10 outputs a fixed voltage of about 1.3V by dividing a power voltage (V_{cc}), the attenuator 20 comprises at least one PIN diode or short key diode (schottky diode), in one embodiment. The provided resistance and voltage values above and in the rest of the disclosure are exemplary. It is noteworthy that the invention should not be construed as limited to such values or approximations.